

**IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Nicholas J. Kelsey, et al.
SERIAL NO.: 09/748,098
FILING DATE: December 21, 2000
TITLE: Instruction Level Multithreading in an Embedded Processor Using
Zero-Time Context Switching
EXAMINER: David J. Huisman
GROUP ART UNIT: 2183
ATTY. DKT. NO.: 20880-05093

CERTIFICATE OF ELECTRONIC (EFS-WEB) TRANSMISSION

I hereby certify that this correspondence is being transmitted via the Office electronic filing system in accordance with 37 C.F.R. § 1.8(a)(i)(C) from the Pacific Time Zone of the United States on the local date shown below.

Dated: February 12, 2010

By: /Brian G. Brannon/

Brian G. Brannon, Reg. No. 57,219

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

AMENDMENT K

SIR:

Responsive to the Office Action dated August 12, 2009, please enter the following amendments, and consider the accompanying remarks. Applicant respectfully requests reconsideration of the claims in the above-identified application in view of the following amendments and remarks for the purpose of putting the application in condition for allowance.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Drawings begin on Page 4 of this paper.

Amendments to the Claims begin on page 5 of this paper.

Remarks begin on page 15 of this paper.